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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/925,278
	Filing Date	8/8/2001
	First Named Inventor	Subhasish Mitra
	Art Unit	2133
	Examiner Name	R. Stephen Dildine Jr.
Total Number of Pages in This Submission	Attorney Docket Number	S00-086/US

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Technology Center (TC)
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<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	
<input checked="" type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> Request for Refund	
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> CD, Number of CD(s) _____	
<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks	
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	Ron Jacobs Reg No. 50,142	
Signature		
Date	2-2-05	

CERTIFICATE OF TRANSMISSION/MAILING		
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.		
Typed or printed name	SYLVIA LEE	
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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In the United States Patent and Trademark Office

Application No: 09/925278
Filed: 8/8/2001
Title: Word Voter for Redundant Systems
Applicant(s): Subhasish Mitra
Examiner: R. Stephen Dildine Jr.
Art Unit: 2133

Mailed 2-2-05
Palo Alto, CA

Information Disclosure Statement

Commissioner of Patents and Trademarks
Alexandria, VA 22313

Dear Sir or Madam:

Attached is a completed Form PTO-1449 and copies of the pertinent parts of the references cited thereon. It is requested that the document(s) on the enclosed form be made of record.

Part I (Authority)

This statement is filed pursuant to:

☒ 37 C.F.R. § 1.97(b).

This information disclosure statement is filed either (1) within three months of the filing date of the national applications; (2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application; or (3) before the mailing date of a first office action on the merits, whichever event occurs last.

Accordingly, this information disclosure statement requires no fee and no certification.

☐ 37 C.F.R. § 1.97(c).

This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97(b), but before the mailing date of either (1) a final action under 37 C.F.R. § 1.113 or (2) a notice of allowance under 37 C.F.R. § 1.311.

Accordingly, this information disclosure statement requires either the fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c) (\$180), or a certification according to 37 C.F.R. § 1.97(e).

☐ 37 C.F.R. § 1.97(d).

This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97(c).

Accordingly, this information disclosure statement requires the petition fee specified in 37 C.F.R. § 1.17(i)(1) to consider an information disclosure statement under 37 C.F.R. § 1.97(d) (\$180), a certification according to 37 C.F.R. § 1.97(e), and a petition requesting consideration of the information disclosure statement.

Conditional Petition

It is respectfully requested that this information disclosure statement be considered, good cause being presented in Part III herein (certification). Please treat this paper as the required petition.

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. § 1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record.

To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.

Part II (Payment)

A check is enclosed as indicated:

- ☒ (X) No fee is due.
- ☐ () The fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c) is enclosed (\$180).
- ☐ () The petition fee specified in 37 C.F.R. § 1.17(i)(1) to consider an information disclosure statement under 37 C.F.R. § 1.97(d) is enclosed (\$180).

Part III (Certification)

Pursuant to 37 C.F.R. § 1.97(e), I certify:

- ☒ (X) No certification is necessary.
- ☐ ()
 - (1) Each item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement.
 - ☐ () The "communication from a foreign patent office" referred to in the certification is an International Search Report, possibly issued by the U.S. Patent and Trademark Office in its capacity as an International Search Authority or International Preliminary Examining Authority.
 - ☐ () The "counterpart foreign application" referred to in the certification corresponds to an ancestor or descendent application of the application for which this information disclosure statement is filed.
- ☐ () (2) No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c), more than three months prior to the filing of the statement.

Part IV (Additional Statement)

An additional statement regarding these items of information () is, (X) is not, enclosed.

Copies of the cited documents (X) are enclosed, () are of record in parent application Serial No. _____ and will be provided if the Examiner deems it convenient.

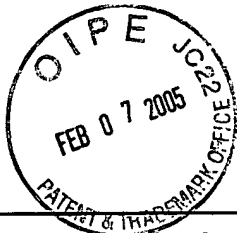
Please note that the references were submitted in the first IDS. Apparently they were not received by the USPTO or missing in the record. Therefore, copies of the references listed are herewith re-submitted.

Respectfully submitted,

Dated: 2-2-05



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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE										ATTY. DOCKET NO. S00-086/US		SERIAL NO. 09/925,278	
LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary)										APPLICANT Subbhasish Mitra			
										FILING DATE 8/8/2001		GROUP 2133	
U.S. PATENT DOCUMENTS													
EXAMINER INITIAL		DOCUMENT NUMBER				DATE		NAME			RELEVANT INFORMATION		
A													
FOREIGN PATENT DOCUMENTS													
		2- letter code		DOCUMENT NUMBER		DATE		COUNTRY			TRANSLATION		
											YES		NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)													
B		[De Sousa 78] De Sousa, P.T., and F.P. Mathur, "Sift-Out-Modular Redundancy," IEEE Trans. Computers, Vol. C-27, No. 7, pp. 624-627, July 1978											
C		[Dennis 74] Dennis, N. G., "Ultra-reliable Voter Switches with a Bibliography of Mechanization," Microelectronics and Reliability, pp. 299-308, August 1974											
D		[Gersting 91] Gersting, J. L., <i>et al.</i> , "A Comparison of Voting Algorithms for N-Version Programming," <i>Proc. Annual Hawaii Intl. Conf. on System Sciences</i> , pp. 253-262, 1991.											
E		[Lala 94] Lala, J. H. and R. E. Harper, "Architectural Principles for Safety-critical Real-time Applications," <i>Proc. of the IEEE</i> , Vol. 82, No. 1, pp. 25-40, Jan. 1994.											
F		[Mitra 99] Mitra, S., N. R. Saxena and E. J. McCluskey, "A Design Diversity Metric and Reliability Analysis for Redundant Systems," <i>Proc. Intl. Test Conf.</i> , pp. 662-671, 1999.											
G		[Mitra 00] Mitra, S., and E. J. McCluskey, "Design of Redundant Systems Protected Against Common-Mode Failures," <i>Technical Report, Center for Reliable Computing, Stanford Univ., CRC-TR-00-2</i> , 2000. (http://crc.stanford.edu)											
H		[Reed 97] Reed, R., <i>et al.</i> , "Heavy Ion and Proton-Induced Single Event Multiple Upset," <i>IEEE Trans. on Nuclear Science</i> , Vol. 44, No. 6, pp. 2224-2229, July 1997.											
I		[Siewiorek 75] Siewiorek, D. P., "Reliability Modeling of Compensating Module Failures in Majority Voted Redundancy," <i>IEEE Trans. Computers</i> , Vol. 24, No. 5, pp. 525-533, 1975.											
J		[Siewiorek 92] Siewiorek, D. P. and R. S. Swarz, <i>Reliable Computer Systems: Design and Evaluation</i> , Digital Press, 1992.											
K		[TI 76] <i>The TTL Data Book</i> , Texas Instruments, 1976.											
L		[Trivedi 82] Trivedi, K.S., <i>Probability and Statistics with Reliability, Queuing, and Computer Science Applications</i> , Prentice Hall, Englewood Cliffs, NJ, USA, 1982.											
M		[Von Neumann 56] Von Neumann, J., "Probabilistic Logics and the Synthesis of Reliable Organisms from Unreliable Components," <i>Automata Studies, Ann. of Math. Studies</i> , no. 34, C. E. Shannon and J. McCarthy, Eds., Princeton University Press, pp. 43-98, 1956.											

	N	[Wong 96] Wong, K., <i>et al.</i> , "A VLSI Median Voter for Fault Tolerant Signal Processing Applications," <i>Proc. Intl. Conf. Signal Processing</i> , pp. 1574-1577, 1996.
	O	[Shehata] et al. "Rapid Prototyping of High Performance DSP Architectures Targeting FPGAs" in a paper published with Concordia University, Dept. of Electrical and Computer Engineering.
EXAMINER		DATE CONSIDERED
<p>* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>		